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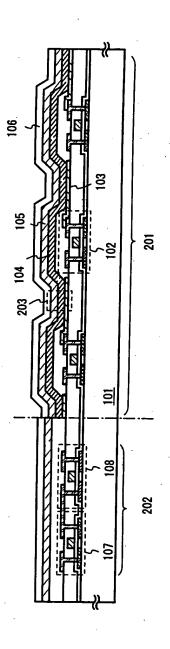
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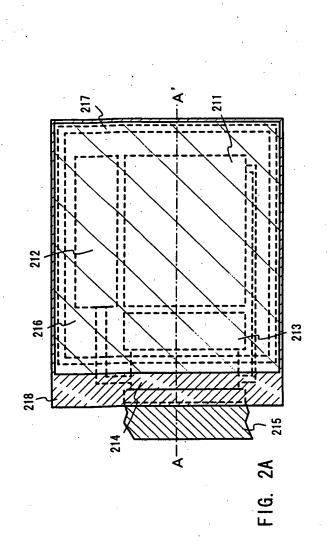
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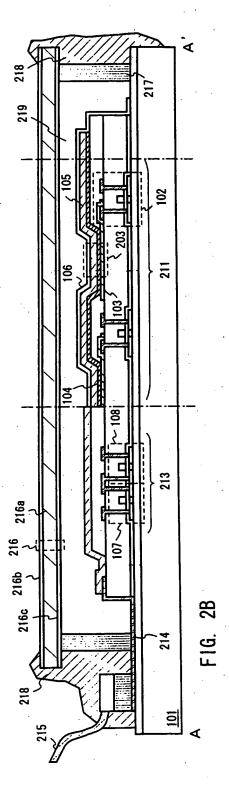
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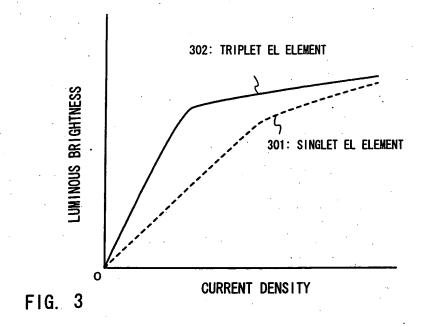
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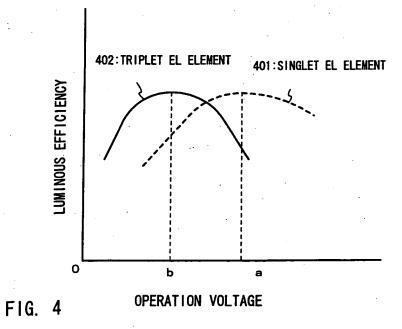




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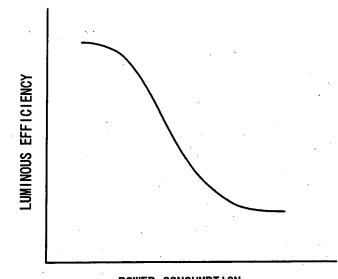
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POWER CONSUMPTION FIG. 5

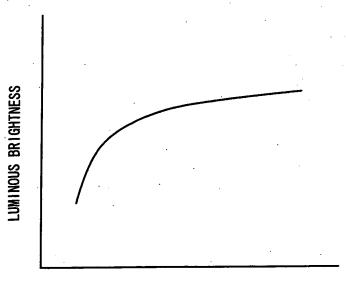
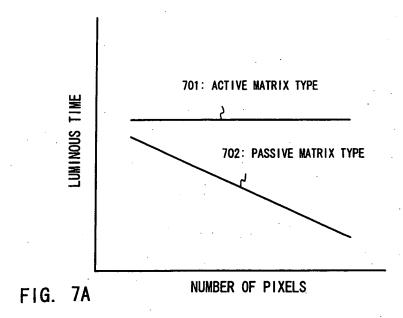


FIG. 6 POWER CONSUMPTION

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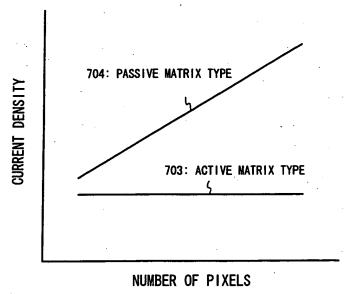


FIG. 7B

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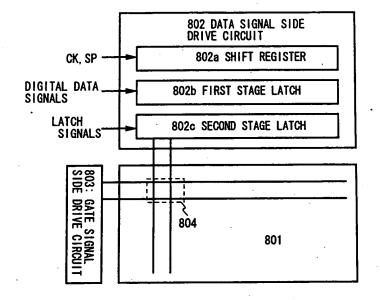


FIG. 8A

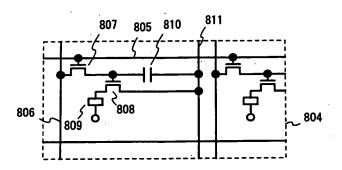


FIG. 8B

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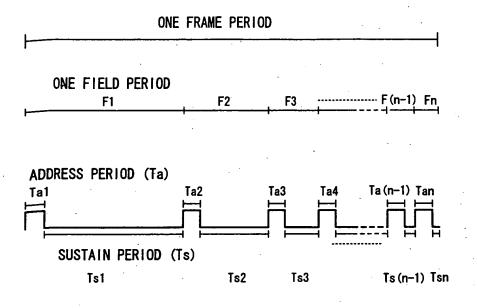


FIG. 9

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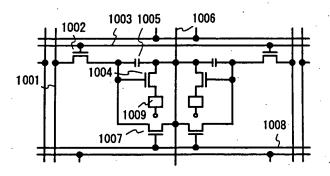


FIG. 10

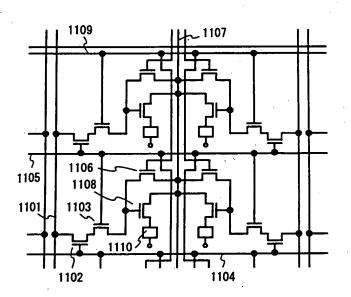


FIG. 11

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FIG. 12A

TIMING CHART OF GATE SIGNALS WHEN A DATA SIGNAL IS WRITTEN IN A PIXEL

CLOCK SIGNAL

(i-1) th GATE SIGNAL

i th GATE SIGNAL

(i+1) th GATE SIGNAL

FIG. 12B

TIMING CHART OF GATE SIGNALS WHEN A DATA SIGNAL IS ERASED FROM A PIXEL

CLOCK SIGNAL

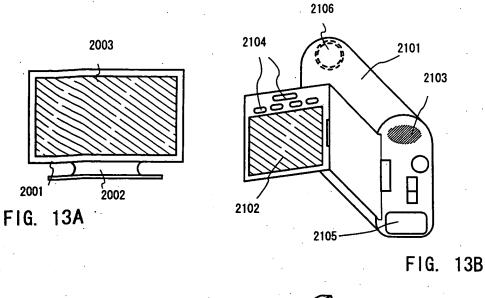
(i-1) th GATE SIGNAL

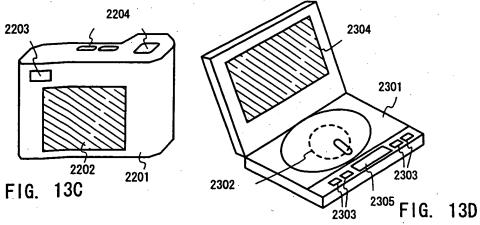
i th GATE SIGNAL

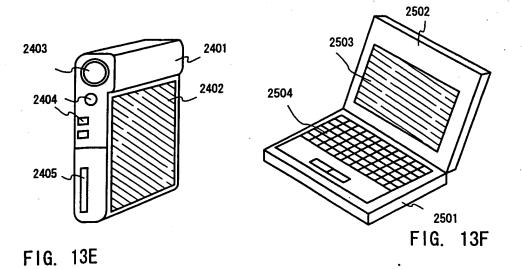
(i+1) th GATE SIGNAL

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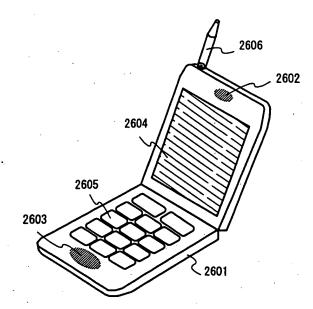


FIG. 14A

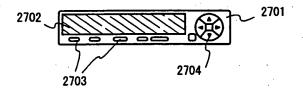
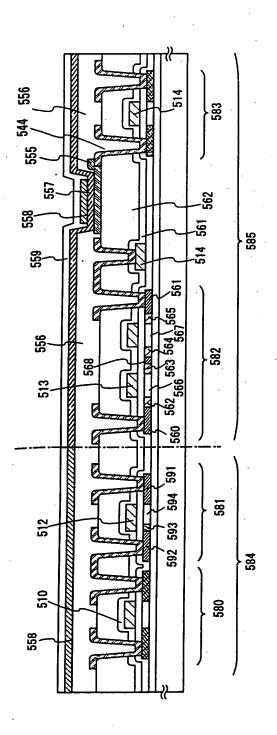


FIG. 14B

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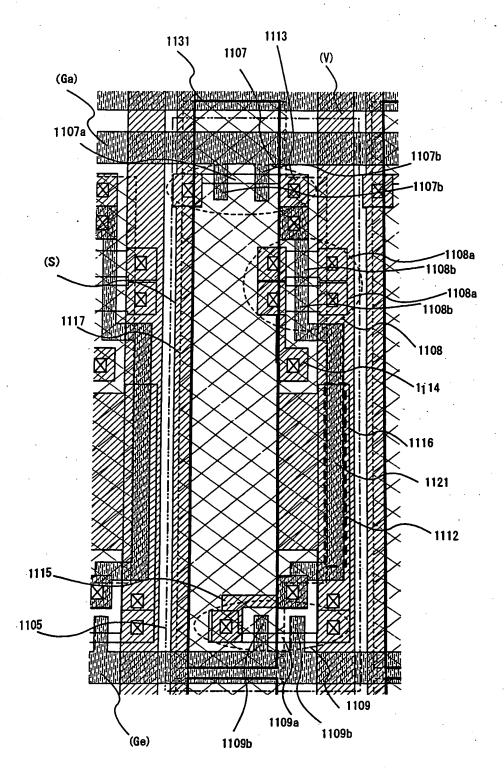


FIG. 16

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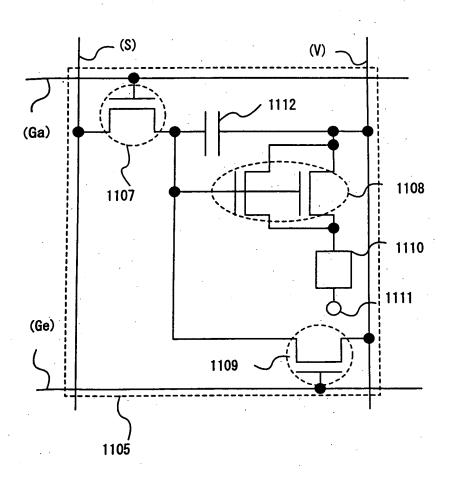


FIG. 17

LIGHT EMITTING DEVICE AND ELECTRICAL APPLIANCE

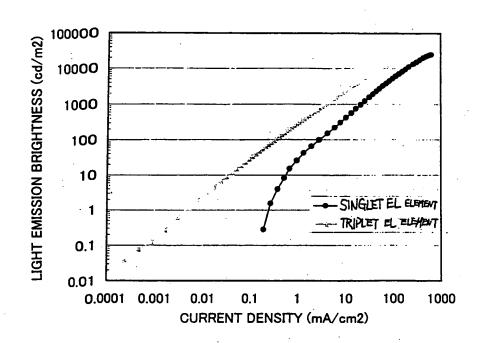


FIG. 18

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LIGHT EMITTING DEVICE AND ELECTRICAL APPLIANCE

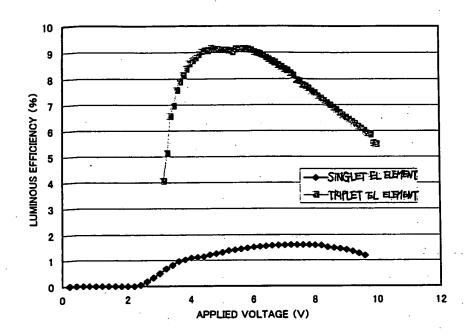


FIG. 19

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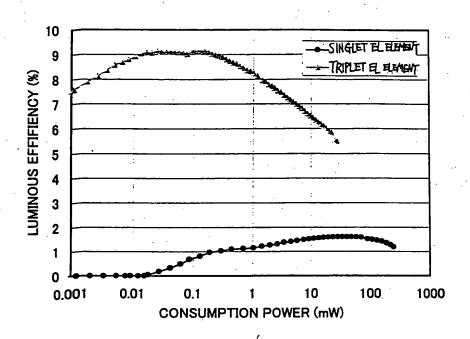


FIG. 20

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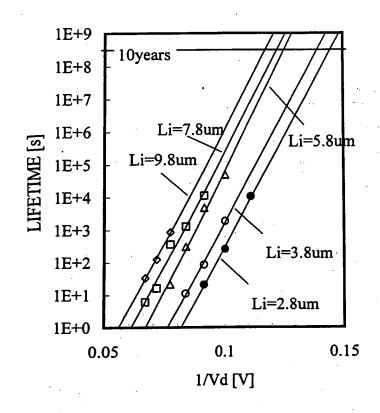


FIG. 21

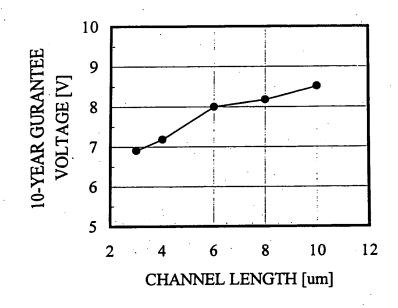


FIG. 22